

SILICON LDMOS MICROWAVE POWER TRANSISTOR 80 W, up to 500 MHz, Enhancement Mode

Designed for broadband commercial and industrial applications with frequencies from to 500 MHz.

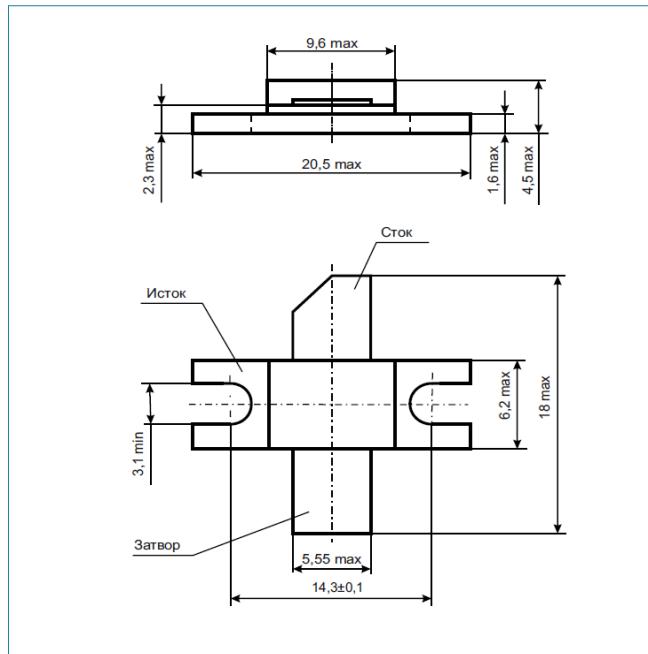
Features:

- Power Gain: 17 dB Min
- Output Power: 80 W
- Efficiency: 65 % Min

Absolute Maximum Ratings

Parameters	Sym	Value	Unit
Drain-Source Voltage	V _{DSS}	65	V _{DC}
Drain Current-Continuous	I _D	7	A _{DC}
Gate-Source Voltage	V _{GS}	-0.5, +20	V _{DC}
Operation Junction Temperature	T _j	-65 ÷ +200	°C
Storage Temperature Range	T _{STG}	-65 ÷ +150	°C
Thermal Resistance, Junction to Case	R _{θJC}	1.0	°C /W
Total Power Dissipation	P _D	175	W

KT-55C-1



Parameters

Parameter	Symbol	Min.	Typ.	Max.	Unit
Drain-Source Breakdown Voltage (I _{DS} =20 mA, V _{GS} =0 V)	V _{(BR)DSS}	65	—	—	V _{DC}
Gate-Source Leakage Current (V _{GS} =20 V, V _{DS} =0 V)	I _{GSS}	—	—	2	μA _{DC}
Zero Gate Voltage Drain Leakage Current (V _{DS} = 28 V, V _{GS} =0 V)	I _{DSS}	—	—	1	mA _{DC}
Gate Threshold Voltage (V _{DS} = 10 V, I _D = 100 mA)	V _{GS(TH)}	2	—	5	V _{DC}
Forward Transconductance (V _{DS} = 10 V, I _D = 1.5 A)	G _{FS}	—	3.0	—	mhos
Drain-Source on-state resistance (V _{GS} = 10 V, I _D = 1.0 A)	R _{DSon}	—	0.18	—	Ω
Dynamic Characteristics					
Input Capacitance (V _{DS} = 28 V, V _{GS} =0 V, f = 1 MHz)	C _{ISS}	—	75	—	pF
Output Capacitance (V _{DS} = 28 V, V _{GS} =0 V, f = 1 MHz)	C _{OSS}	—	50	—	pF
Reverse Transfer Capacitance (V _{DS} = 28 V, V _{GS} =0 V, f = 1 MHz)	C _{RSS}	—	1.7	—	pF
Functional Characteristics					
Power Gain (V _{DS} = 28 V, P _{OUT} = 80 W, I _{DQ} = 100 mA, f = 500 MHz)	G _p	17	19	—	dB
Drain Efficiency (V _{DS} = 28 V, P _{OUT} = 80 W, I _{DQ} = 100 mA, f = 500 MHz)	η _D	65	70	—	%

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Specification are subject to change without notice