

SILICON BIPOLAR NPN POWER TRANSISTOR 200 W, in the 30 – 175 MHz Frequency Range

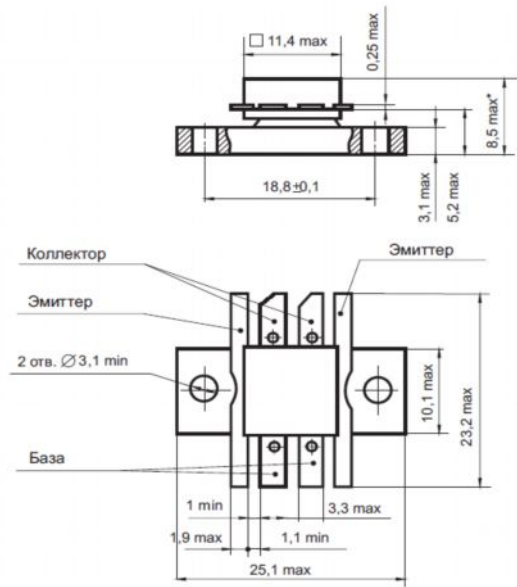
The silicon bipolar n-p-n transistor is designed for wideband large-signal output and driver amplifier stages in the 30 to 175 MHz frequency range.

Features (At 175 MHz):

- Output Power: 200 W
- Power Gain: 7.5 dB Min
- Efficiency: 60% Min

Absolute Maximum Ratings

Parameters	Sym	Value	Unit
Collector-Base Voltage	V_{CBO}	50	V_{DC}
Emitter-Base Voltage	V_{EBO}	4	V_{DC}
Collector Current	I_C	18	A_{DC}
Operation Junction Temperature	T_j	$-65 \div +200$	$^{\circ}C$
Storage Temperature Range	T_{STG}	$-65 \div +150$	$^{\circ}C$
Thermal Resistance, Junction to Case	$R_{\theta JC}$	0.61	$^{\circ}C/W$
Total Power Dissipation, $T_C=25^{\circ}C$	P_D	287	W



CaseKT-45

Parameters

Parameter	Symbol	Min.	Typ.	Max.	Unit
Collector-Emmitter Breakdown Voltage ($I_C = 50$ mA, $V_{BE} = 0$ V)	$V_{(BR)CER}$	50	—	—	V_{DC}
Emitter-Base Breakdown Voltage ($I_E = 5$ mA, $I_C = 0$ A)	$V_{(BR)EBO}$	4	—	—	V_{DC}
Collector-Base Leakage Current ($V_{CB} = 30$ V, $I_E = 0$ A)	I_{CBO}	—	—	20	mA_{DC}
DC Current Gain ($V_{CE} = 5$ V, $I_C = 1$ A)	h_{FE}	20	—	100	
Output Capacitance ($V_{CB} = 28$ V, $I_E = 0$ A, $f = 1$ MHz)	C_{OB}	—	—	430	pF
Power Gain ($V_{CC} = 28$ V, $f = 175$ MHz, $P_{OUT} = 200$ W)	Gp	7.5	12	—	dB
Drain Efficiency ($V_{CC} = 28$ V, $f = 175$ MHz, $P_{OUT} = 200$ W)	η	60	—	—	%

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Specification is subject to change without notice